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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Fabrice BLISSON and Gilles DEBEURME

Serial No:

10/774,896

Filed:

February 9, 2004

For:

CIRCUIT WITH AN INTEGRATED VOLTAGE REGULATOR AND IT

MANUFACTURING PROCESS

Examiner:

Kevin V. Quinto

Art Unit:

2826

Confirmation No.:

8769

MAIL STOP RCE

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir/Madam:

Prior to examination, please amend this application as follows:

A complete Listing of the Claims in this application begins on page 2 of this paper.

Remarks begin on page 7 of this paper.

PTO/88/06 (08-03)

Approved for use through 7/31/2008. OMB 0651-0032

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